

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B150113813 CHAUDHARI PUSHPAK CHANDRAKANT RAJASHREE ,70927643F ,CEPN ,B150113813

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 19 | 29 | 48 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 13 | 29 | 42 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 14 | 29 | 43 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 19 | 35 | 54 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 12 | 24 | -- | F |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 40 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 42 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 42 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 42 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 43 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

B150113814 GADEKAR YOGESH SHASHIKANT SUMAN ,71203313M ,CEPN ,B150113814

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 25 | 28 | 53 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 17 | 28 | 45 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 17 | 28 | 45 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 23 | 30 | 53 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 23 | 39 | 62 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 44 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 42 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 45 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 45 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 45 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = 479/750 , EARN CREDITS :

B150113815 GAGARE MADHURI ARUN ANITA ,71403865C ,CEPN ,B150113815

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 18 | 28 | 46 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 19 | 29 | 48 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 22 | 34 | 56 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 22 | 40 | 62 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 20 | 48 | 68 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 42 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 40 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 41 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 41 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = 487/750 , EARN CREDITS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B150113816 GAVATE VISHAL SONYABAPU KAMAL ,71403877G ,CEPN ,B150113816

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 17 | 17 | -- | F |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 16 | 28 | 44 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 16 | 28 | 44 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 18 | 35 | 53 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 07 | 23 | -- | F |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 41 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 38 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 40 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 38 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 38 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

B150113817 GIRI SURAJ SHRIDHAR SARITA ,71403886F ,CEPN ,B150113817

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 18 | 30 | 48 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 13 | 31 | 44 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 22 | 37 | 59 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 26 | 40 | 66 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 16 | 41 | 57 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 42 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 40 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 42 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 41 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = 482/750 , EARN CREDITS :

B150113818 LANDE MEGHA KISHOR REKHA ,71634789L ,CEPN ,B150113818

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 18 | 28 | 46 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 11 | 25 | -- | F |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 21 | 28 | 49 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 24 | 20 | -- | F |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 14 | 28 | 42 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 42 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 39 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 41 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 42 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B150113819 MAGAR RAJARAM BHASKAR KUSUM ,71070343M ,CEPN ,B150113819

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 14 | 19 | -- | F |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 15 | 22 | -- | F |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 13 | 30 | 43 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 17 | 39 | 56 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 12 | 35 | 47 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 40 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 38 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 40 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 38 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 38 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

B150113820 MANDAVE SONAL SUDHIR SWAGATA ,71634793J ,CEPN ,B150113820

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 20 | 32 | 52 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 16 | 32 | 48 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 20 | 40 | 60 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 15 | 36 | 51 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 18 | 36 | 54 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 40 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 39 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 44 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 42 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 41 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = 471/750 , EARN CREDITS :

B150113821 MULANI SAMEER HUSAIN SALIMA ,71304014K ,CEPN ,B150113821

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 20 | 08 | -- | F |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 19 | AA | -- | F |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 23 | 38 | 61 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 23 | 49 | 72 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 13 | 46 | 59 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 39 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 37 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 41 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 40 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B150113822 NIMBALKAR AKSHAY JALINDAR SUNANDA ,71203484G ,CEPN ,B150113822

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 19 | 31 | 50 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 18 | 19 | -- | F |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 22 | 45 | 67 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 22 | 48 | 70 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 22 | AA | -- | F |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 41 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 38 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 42 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 43 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

B150113823 PACHPIND RAHUL BHAUSAHEB SUREKHA ,71451120L ,CEPN ,B150113823

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 14 | 11 | -- | F |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 10 | 28 | -- | F |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 18 | AA | -- | F |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 17 | AA | -- | F |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 09 | AA | -- | F |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 37 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 35 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 40 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 38 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 42 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = --/750 , EARN CREDITS :

B150113824 PHUTANE SHUBHAM VIJAY SUREKHA ,71501338G ,CEPN ,B150113824

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 19 | 28 | 47 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 19 | 28 | 47 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 19 | 30 | 49 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 18 | 32 | 50 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 18 | 37 | 55 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 43 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 42 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 41 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 44 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = 461/750 , EARN CREDITS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B150113825 SHINDE DEEPALI RAGHUNATH BEBY ,71634802M ,CEPN ,B150113825

SEM.:1

| | | | | | | | | |
|---------|---------------------------|----|-----|----|----|----|----|---|
| 404201 | VLSI DESIGN | PP | 100 | 40 | 22 | 32 | 54 | P |
| 404202 | ADVANCED POWER ELECTRONIC | PP | 100 | 40 | 12 | 30 | 42 | P |
| 404203 | ELECTRONICS SYSTEM DESIGN | PP | 100 | 40 | 20 | 38 | 58 | P |
| 404204C | EMBEDDED SYSTEMS & RTOS | PP | 100 | 40 | 23 | 47 | 70 | P |
| 404205A | MOBILE COMMUNICATION | PP | 100 | 40 | 22 | 39 | 61 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | TW | 50 | 20 | | | 40 | P |
| 404206 | LAB PRACTICE -I (APE+ ESD | PR | 50 | 20 | | | 41 | P |
| 404207 | LAB PRACTICE -II (VLSI + | TW | 50 | 20 | | | 43 | P |
| 404207 | LAB PRACTICE -II (VLSI + | PR | 50 | 20 | | | 43 | P |
| 404208 | PROJECT STAGE I | OR | 50 | 20 | | | 42 | P |
| 414461C | CRITICAL THINKING | AC | 0 | 0 | | | PP | P |

FIRST SEM TOTAL = 494/750 , EARN CREDITS :